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To:	Examiner Long K. Tran	From:	Leslie S. Szivos
Fax:	(571) 273-8300	Pages:	6 pages including cover sheet
Phone:		Date:	8/29/2005
Re:	U.S. Serial No: 10/709,450 Group Art Unit: 2818 Confirmation No: 3449 Docket No: FIS920030366US1 (17138)	CC:	

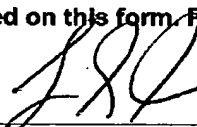
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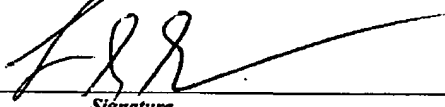
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TRANSMITTAL LETTER (General - Patent Pending)				Docket No. FIS920030366US1 (17138)	
In Re Application Of: James W. Addisson, et al.					
Application No. 10/709,450	Filing Date May 6, 2004	Examiner Long K. Tran	Customer No. 23389	Group Art Unit 2818	Confirmation No. 3449
Title: ULTRA-THIN SI MOSFET DEVICE STRUCTURE AND METHOD OF MANUFACTURE					
<u>COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is: RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143					
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 _____ Signature			Dated: August 29, 2005		
Leslie S. Szivos Registration No. 39,394					
SCULLY, SCOTT, MURPHY & PRESSER 400 Garden City Plaza, Suite 300 Garden City, New York 11530 (516) 742-4343					
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CENTRAL FAX CENTER****AUG 29 2005****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****Applicant(s):** James W. Adkisson, et al.**Examiner:** Long K. Tran**Serial No:** 10/709,450**Art Unit:** 2818**Filed:** May 6, 2004**Docket:** FIS920030366US1 (17138)**For:** OUT OF THE BOX VERTICAL
TRANSISTOR FOR eDRAM ON SOI**Dated:** August 29, 2005**Confirmation No.:** 3449Mail Stop Amendment
Commissioner for Patents
United States Patent Office
Alexandria, VA 23313-1450**RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143****Sir:**

In response to the Office Action dated August 15, 2005, applicants provisionally elect, without traverse, the subject matter of Group I, i.e., Claims 1-10 for continued prosecution in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: August 29, 2005
Leslie S. Szivos